RPS920010132US1 R. T. Bailis et al. Method and System for Use of a Field Programmable Gate Array (FPGA) Cell for Controlling Access to OnChip Functions of a System on A Chip (SOC)...

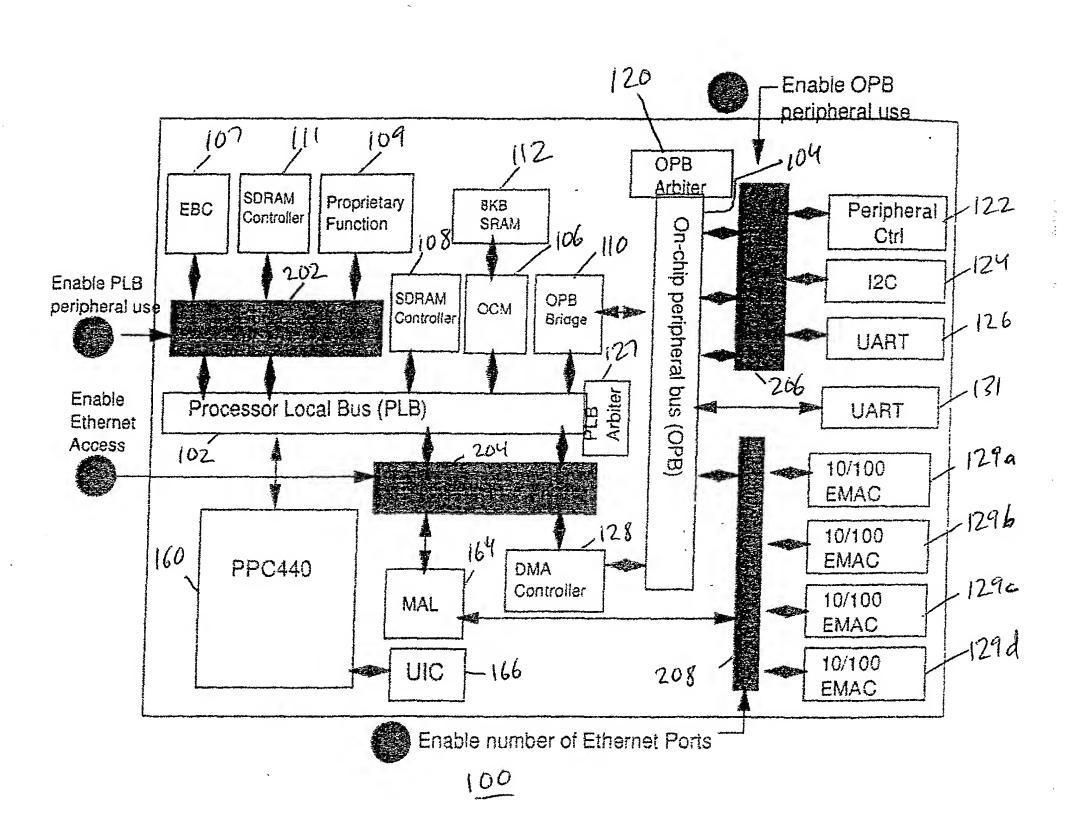


Fig. 1.

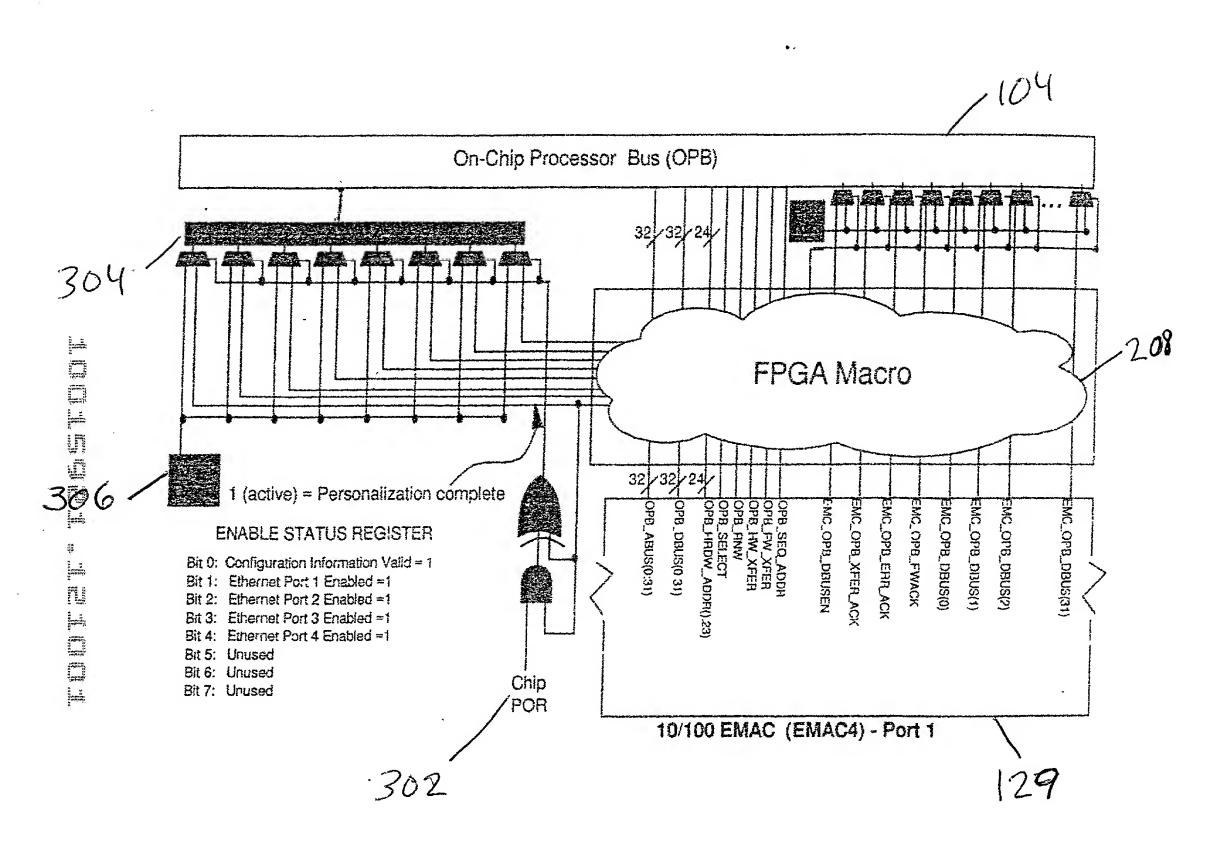


Fig. 2